



Friedrich-Alexander-Universität
Chair of Electron Devices



**Forge for young master craftsmen and craftswomen
in semiconductor engineering and technology**

www.leb.tf.fau.de/bauhaus

**Training along the process chain: Design, development,
manufacturing and testing of devices**



Fraunhofer IISB: The SiC semiconductor workshop

- Professional SiC pilot line for specialized chips
- Semiconductor workshop for trained young masters of the μ E-bauhaus erlangen-nürnberg
- Training like at the workbench – application-oriented and close to industry

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